



<b>PETITION FOR REVIVAL OF AN APPLICATION FOR PATENT ABANDONED UNAVOIDABLY UNDER 37 CFR 1.137(a)</b>		<b>Docket Number (Optional)</b> 200310972-1
First Named Inventor: Shaun L. Harris	Art Unit: N/A	
Application Number: 10/737,045-Conf. #4754	Examiner: Not Yet Assigned	
Filed: December 16, 2003		
Title: SYSTEM AND METHOD FOR POWER DISTRIBUTION		
 MS Petition Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 NOTE: If information or assistance is needed in completing this form, please contact Petitions Information at (571) 272-3282.		
The above-identified application became abandoned for failure to file a timely and proper reply to a notice or action by the United States Patent and Trademark Office. The date of abandonment is the day after the expiration date of the period set for reply in the Office notice or action plus any extensions of time actually obtained.		
<b>APPLICANT HEREBY PETITIONS FOR REVIVAL OF THIS APPLICATION.</b> NOTE: A grantable petition requires the following items: (1) Petition fee. (2) Reply and/or issue fee. (3) Terminal disclaimer with disclaimer fee – required for all utility and plant applications filed before June 8, 1995, and for all design applications; and (4) Adequate showing of the cause of unavoidable delay.		
1. Petition fee		
<input type="checkbox"/> Small entity – fee \$ _____ (37 CFR 1.17(l)). Applicant claims small entity status. See 37 CFR 1.27.		
<input checked="" type="checkbox"/> Other than small entity – fee \$ <u>500.00</u> (37 CFR 1.17(l)).		
2. Reply and/or fee		
A. The reply and/or fee to the above-noted Office action in the form of <u>the issue fee and Issue Fee Transmittal, Part B</u> (identify the type of reply):		
<input type="checkbox"/> has been filed previously on _____		
<input checked="" type="checkbox"/> is enclosed herewith.		
B. The issue fee of \$ <u>1,700.00</u>		
<input type="checkbox"/> has been filed previously on _____		
<input checked="" type="checkbox"/> is enclosed herewith.		

12/14/2005 HUONG2 00000013 062380 10737045  
03 FC:1452 500.00 DA

**PETITION FOR REVIVAL OF AN APPLICATION FOR PATENT ABANDONED  
UNAVOIDABLY UNDER 37 CFR 1.137(a)**

## 3. Terminal disclaimer with disclaimer fee

☒ Since this utility/plant application was filed on or after June 8, 1995, no terminal disclaimer is required.☐ A terminal disclaimer (and disclaimer fee (37 CFR 1.20(d)) of \$ \_\_\_\_\_ for a small entity or \$ \_\_\_\_\_ for other than a small entity) disclaiming the required period of time is enclosed herewith (see PTO/SB/63).

## 4. An adequate showing of the cause of the delay, and that the entire delay in filing the required reply from the due date for the reply until the filing of a grantable petition under 37 CFR 1.137(a) was unavoidable, is enclosed.

  
Signature

December 9, 2005

Date

Jerry L. Mahurin  
Typed or printed name34,661  
Registration Number, if applicableFULBRIGHT & JAWORSKI L.L.P.  
2200 Ross Avenue, Suite 2800  
Dallas, Texas 75201-2784

Address

(214) 855-8386  
Telephone NumberEnclosure ☒ Fee Payment☒ Reply☐ Terminal Disclaimer Form☒ Additional sheets containing statements establishing unavoidable delay☐ \_\_\_\_\_

**PETITION FOR REVIVAL OF AN APPLICATION FOR PATENT ABANDONED  
UNAVOIDABLY UNDER 37 CFR 1.137(a)**

**NOTE:** The following showing of the cause of unavoidable delay must be signed by all applicants or by any other party who is presenting statements concerning the cause of delay.

 _____ Signature	December 12, 2005 _____ Date
Mahurin, Jerry L. _____ Typed or printed name	34,661 _____ Registration Number, if applicable

(In the space provided below, please explain in detail the reasons for the delay in filing a proper reply.)

MPEP §711.03(c)(III)(C) provides that a showing of unavoidable delay will require: (1) evidence concerning the procedures in place that should have avoided the error resulting in the delay; (2) evidence concerning the training and experience of the persons responsible for the error; and (3) copies of any applicable docketing records to show that the error was in fact the cause of the delay. To this end please find the following attached as Exhibits 1-4.

Exhibit 1 is a copy of the IPT section of the Dallas Office of Fulbright and Jaworski's completed "Checklist and Procedure for Hewlett Packard Notice of Allowance" for this matter. (Please note, Hewlett Packard Company is the assignee of the present application.) Attention is respectfully directed to item 1, under the second heading of "Secretary," appearing in the lower portion of the page, which provides, in part, that a letter to Hewlett Packard should be prepared (reporting the findings of the review) and forwarded to the Hewlett Packard Handling Attorney.

Exhibit 2 is a copy of a letter prepared in accordance with the procedures of Exhibit 1 which was docketed by Fulbright and Jaworski, as discussed in greater detail below, but which was never forwarded to Hewlett Packard advising Hewlett Packard that the case was in condition for the Issue Fee to be paid. Applicant respectfully asserts that Exhibits 1 and 2 together provide sufficient evidence concerning the procedures in place that should have avoided the error resulting in delay of payment of the Issue Fee in the present case.

Exhibit 3 is the Declaration of Elise Perkins, an IP Secretary at Fulbright and Jaworski, who was responsible for completing item 1 under the heading "Secretary" in the attached Exhibit 1. This Declaration is believed to provide the necessary evidence concerning the training and experience of the persons responsible for the error, as well as reinforce the evidence concerning the procedures in place that should have avoided the error resulting in delay of payment of the Issue Fee in the present case provided by Exhibits 1 and 2.

The final Exhibit, Exhibit 4 is a screen-shot of an "actions" page of the docket for the present application in Fulbright and Jaworski's IP docket system. Exhibit 4 shows the erroneous entry in the docket of the review of the Notice of Allowance being completed in this case on October 10, 2005, well in advance of the Issue Fee due date of November 30, 2005. This entry in the docket should have only been made once the Notice of Allowance review letter had been sent to the client, Hewlett Packard, advising Hewlett Packard that the case was in condition for the Issue Fee to be paid.

(cont'd.)

*(Please attach additional sheets if additional space is needed)*

Applicant respectfully asserts that, in accordance with MPEP §711.03(c)(III)(C)(2), Exhibits 1 through 4 establish that the delay in paying the Issue Fee in this case resulted from an error on the part of an employee of Fulbright & Jaworski in the performance of clerical functions and thus provides a basis for a showing of "unavoidable" delay. The Exhibits establish that the error was the cause of the delay at issue, in that Hewlett Packard was never informed that the application was in condition for the Issue Fee to be paid. The Exhibits further establish that a business routine was in place for performing the clerical function that could reasonably be relied upon to avoid errors in its performance. Finally, the Exhibits establish that the employee was sufficiently trained and experienced with regard to the function and routine for its performance that reliance upon such employee represented the exercise of due care on the part of Fulbright and Jaworski, as attorneys for Hewlett Packard.

HP DOCKET NO. 2003109-1F&J DOCKET NO. 4760 P 788US 10310561**RETAIN WITH FILE****Checklist And Procedure For Hewlett-Packard Notice Of Allowance****DOCKET CLERK:**

- [ ] 1. Receive Notice of Allowance and RFQ from Hewlett-Packard. DATE: \_\_\_\_\_
- [ ] 2. Place amount on RFQ. Response due to HP: DATE: \_\_\_\_\_
- [ ] 3. File and Notice of Allowance to handling attorney: DATE: \_\_\_\_\_

**SECRETARY:**

- [ep] 1. Are any drawing changes necessary? Y or N If Yes, have they been made? \_\_\_\_\_
- [ep] 2. Prepare Claims As Allowed. Document No. 25578238
- [ep] 3. Inventor Name: Shawn L. Harris, Gary Williams, Rico Brooks
- [ep] 4. Inventor phone number & e-mail: 972-497-4518 shawn.harris@hp.com
- [ep] 5. E-mail clean copy of application and drawings as filed, the Claims As Allowed document, along with a set of the attorney follow-up questions, to the Inventor(s) for review, and request a good day and time for attorney to contact him/her regarding the invention.
- [ep] 6. Set appointment and calendar.

**ATTORNEY:**

- [ep] 1. Follow-up questions:
- Are the claims broad enough and do they cover all aspects of the invention?
  - Have there been any changes to the invention?
  - Do you have any knowledge of infringements?
  - Is there any known prior art to the invention that has not been filed in an IDS?
- [ep] 2. If the answer to any of questions b, c, and d above are yes, or the answer to question a is no, attorney is to follow-up before proceeding with review. If answers to b-d are no, or the answer to question a is yes, go on to following number 3.
- [ep] 3. If there are other claim sets to prosecute, call HP handling attorney to determine if they would like to continue prosecution by filing a continuation application.  
→ If HP handling attorney would like to file a continuation application, must obtain new RFQ.

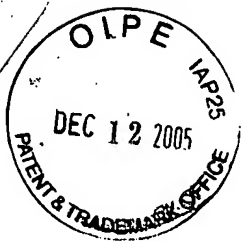
**SECRETARY:**

- [ep] 1. Prepare letter to HP from sample letter. Document no. 25320453  
Attach clean copy of Claims As Allowed document and forward via U.S. mail to HP handling attorney.

**HP LEGAL ASSISTANT:**

- [ep] 1. Is there any prior art in the application or file which has not been cited in an IDS? Y or N
- [ep] 2. If Yes to question above, is an IDS needed? Y or N NA  
If Yes prepare IDS and file with the USPTO.
- [ep] 3. Is Issue Fee to be paid by Fulbright? Y or N
- [ep] 4. Is PTA/PTE accurate? Y or N or NA 195 days
- [ep] 5. Send e-mail to department billing-coordinator to inform that all work is complete for the Notice of Allowance and file is ready to be billed.
- [ep] 6. Give file to Docket Clerk → gone to gail miller

Approved By: [Signature]Date: 10/10/05



## FULBRIGHT & JAWORSKI L.L.P.

A REGISTERED LIMITED LIABILITY PARTNERSHIP

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October 10, 2005

Mr. Bill Streeter  
Hewlett-Packard Company  
3404 East Harmony Road, M/S 79  
Fort Collins, Colorado 80528

Re: U.S. Patent Application  
Application No.: 10/737,045  
Title: SYSTEM AND METHOD FOR POWER DISTRIBUTION  
Filed: December 16, 2003  
Inventor: Shaun L. Harris et al.  
Your Reference: 200310972-1  
Our Reference: 47607/P788US/10310561

Dear Bill:

Pursuant to your request to investigate the current status of the above-identified file, we discussed the allowed claims (copy attached) with the inventor and the following was determined:

- No changes appear to be necessary in the claims as allowed.
- The inventor(s) are not aware of prior art, which has not been disclosed to the U.S. Patent Office in this matter.
- The inventor(s) have no knowledge of infringers.

Further, our review of the file indicates:

- There are no claim sets, which remain to be prosecuted.
- All material prior art in our file for this case has been disclosed to the U.S. Patent Office.
- The drawings filed December 16, 2003 are acceptable.

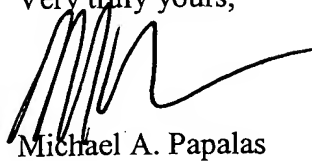
25582943.1

Exhibit 2

Mr. Bill Streeter  
October 10, 2005  
Page 2

If you have any questions, please do not hesitate to contact me.

Very truly yours,

A handwritten signature in black ink, consisting of several loops and a long horizontal stroke at the end.

Michael A. Papalas

MAP/CJC/ep  
Enclosures



## CLAIMS AS ALLOWED

1. A power distribution system for electronic equipment, comprising:  
a voltage bus;  
a plurality of power supplies coupled in parallel to said voltage bus;  
a plurality of diodes wherein each of said diodes is disposed between a power supply and said voltage bus to isolate said voltage bus from a low power supply voltage;  
a plurality of sense lines, wherein each of said sense lines is coupled to one of said plurality of power supplies; and  
a plurality of resistive elements, wherein each of said resistive elements is coupled to said voltage bus and to a respective sense line of said plurality of sense lines, wherein said plurality of resistive elements maintain, when a minimal load is applied to said voltage bus, said sense lines at a voltage sufficiently lower than a voltage of said voltage bus to cause said plurality of power supplies to prevent said plurality of diodes from being reversed-biased.
2. The power distribution system of claim 1, wherein each of said plurality of power supplies comprises a controller for increasing or decreasing a duty cycle in response to feedback associated with a respective sense line of said plurality of sense lines.
3. The power distribution system of claim 2 wherein each power supply of said plurality of power supplies comprises an error amplifier coupled between a respective sense line of said plurality of sense lines and said controller.
4. The power distribution system of claim 1 wherein said plurality of power supplies are brick converters.
5. The power distribution system of claim 1 further comprising:  
a plurality of protective resistors each coupled between a respective output of one of said plurality of power supplies and a respective sense line of said plurality of sense lines.
6. The power distribution system of claim 5 wherein each of said plurality of resistors possesses a resistance that is one order of magnitude greater than resistance of said plurality of resistive elements.

7. A method comprising:  
providing a plurality of power supplies to maintain a voltage on a voltage bus;  
providing a plurality of blocking diodes with each blocking diode being disposed between a respective power supply of said plurality of power supplies and said voltage bus to isolate said voltage bus from a low output voltage;  
coupling a plurality of sense lines to said plurality of power supplies to provide feedback to control said plurality of power supplies; and  
coupling a plurality of resistors with each resistor being disposed between a respective sense line of said plurality of sense lines and said voltage bus, wherein said plurality of resistors maintain said sense lines at a sufficiently low voltage when a low load is applied to said voltage bus to prevent said plurality of blocking diodes from being reversed biased.
8. The method of claim 7 further comprising:  
increasing a duty cycle of one of said plurality of power supplies when a voltage on a respective sense line falls below a reference voltage.
9. The method of claim 7 further comprising:  
decreasing a duty cycle of one of said plurality of power supplies when a voltage on a respective sense line exceeds a reference voltage.
10. The method of claim 7 further comprising:  
operating a respective differential amplifier for each of said power supplies with a first input coupled to a respective sense line and with a second input coupled to a reference voltage.
11. The method of claim 7 wherein said plurality of power supplies are brick converters.
12. The method of claim 7 further comprising:  
providing a second plurality of resistors with each resistor of said second plurality of resistors being coupled between a respective power supply and a respective sense line.
13. The method of claim 12 wherein each resistor of said plurality of resistors possesses a resistance that is approximately 1/10th of a resistance of said second plurality of resistors.

14. A power distribution system for electronic equipment, comprising:  
a bus for supplying current to said electronic equipment;  
a plurality of brick converters coupled in parallel to said bus with a respective blocking diode between each brick converter and said bus;  
a plurality of sense lines providing feedback to respective brick converters; and  
a plurality of resistive elements that are each coupled to said bus and to a respective sense line of said plurality of sense lines, wherein said plurality of resistive elements cause sufficient feedback to be provided to said brick converters, under low load conditions, to prevent output voltages of said plurality of brick converters from falling below a voltage of said bus.
15. The power distribution system of claim 14, wherein each of said plurality of brick converters increases or decreases a duty cycle in response to said feedback.
16. The power distribution system of claim 15 wherein each brick converter of said plurality of brick converters comprises a differential amplifier coupled to a respective sense line and a reference voltage.
17. The power distribution system of claim 16 wherein each brick converter of said plurality of brick converters comprises a controller for processing an error signal from a respective differential amplifier.
18. The power distribution system of claim 16 wherein said reference voltage defines a voltage rail for said electronic equipment.
19. The power distribution system of claim 14 further comprising:  
a plurality of protective resistors each coupled between a respective output of one of said plurality of brick converters and a respective sense line.
20. The power distribution system of claim 19 wherein each of said plurality of resistors possesses a resistance that is one order of magnitude greater than resistance of said plurality of resistive elements.



Docket No.: 200310972-1  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Shaun L. Harris et al.

Application No.: 10/737,045

Confirmation No.: 4754

Filed: December 16, 2003

Art Unit: N/A

For: SYSTEM AND METHOD FOR POWER  
DISTRIBUTION

Examiner: N/A

**DECLARATION OF ELISE PERKINS**

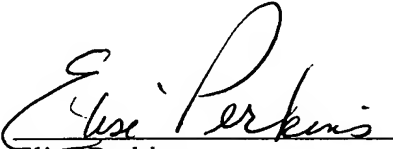
MS Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I, Elise Perkins, declare: I am an Intellectual Property Secretary in the Dallas Office of the law firm Fulbright & Jaworski, LLP. I have reviewed the attached Exhibits 1 and 2. I was the secretary responsible for completion of item 1 under the second "Secretary" heading on the "Checklist and Procedure for Hewlett Packard Notice of Allowance" identified as Exhibit 1 and that my initials appear beside that item indicating completion of that item by me. Exhibit 2 is the letter I prepared and presented to Michael A. Papalas for signature on October 10, 2005 to complete the first portion of item 1. However, rather than mailing the letter, or otherwise transmitting the letter, to the responsible Hewlett Packard Company Attorney, I placed the file and letter in the docketing department in-box for docketing.

I further declare that I have been an Intellectual Property Secretary for over one year, I have been trained to carry out the procedures enumerated in the "Checklist and Procedure for Hewlett Packard Notice of Allowance", I have carried-out these procedures in the past, and have since carried-out these procedures in other cases.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States code and that such willful false statements may jeopardize the validity of the above-identified application or any patent issued thereon.

December 12, 2005  
Date

  
Elise Perkins  
Intellectual Property Secretary  
Fulbright & Jaworski LLP  
2200 Ross Avenue, Suite 2800  
Dallas, TX 75201